

International Journal of Technology and Engineering System(IJTES) ISSN: 0976-1345 Vol5.No.1 2013 pp 41-44 available at: <u>www.ijcns.com</u> Paper Received :15-05-2013 Paper Received by: 1. Prof.S.Satya Janaki 2.Dr.Rajesh Kanna Editor : Prof. P.Muthukumar

### IMPLEMENTATION OF NEW SINGLE PHASE MULTILEVEL INVERTER FOR PV POWER CONDITIONING SYSTEM

\*S.Kirupakaran, \*\*B.S.Manikandaprabu

Asst professor, Department of Electrical Engineering Sri Lakshmi Ammal Engineering College, Anna University, Chennai, India. \*s.chenkirupa@gmail.com.\*\*bsmkprabu@gmail.com

Abstract: This project presents a high conversion ratio hybrid DC-DC converter fed single-phase low harmonic distortion nine-level photovoltaic (PV) inverter topology for PV power conditioning systems with a novel pulse width-modulated (PWM) control scheme. A digital PI control algorithm is implemented in microcontroller PIC16C7F88 to keep the current injected into the grid sinusoidal and to have good dynamic performance with rapidly changing atmospheric conditions. For low-voltage dc energy sources, a power conditioning system (PCS) is needed to convert the energy sources to a higher-voltage dc before making it to ac for grid tie applications. Fuel cells and Solar photovoltaic (PV) are perhaps the most well-known and prospective energy sources with low voltage dc output. A thermoelectric generator, a battery, and an ultra-capacitor are also examples of such lowvoltage dc energy sources. Recently, numerous circuit topologies for the power conversion from low-voltage dc to high-voltage ac for grid-tie applications were proposed to deal with specific issues such as high efficiency, low cost, and safety. The whole system is constructed and simulated using MATLAB/SIMULINK environment. The power qualities under different conditions are given and compared. An experimental prototype of High efficiency power conditioning system was built and Experimental results are shown to verify the effectiveness of the proposed power conditioning topology.

#### I. INTRODUCTION

As The World is concerned with fossil-fuel exhaustion and environmental problems caused by conventional power generation, renewable energy sources, particularly solar and wind energy, have become very popular and demanding. Photovoltaic (PV) sources are used today in many applications because they have the advantages of being maintenance and pollution free. Solar-electricenergy demand has grown consistently by 20%–25% per annum over the past 20 years, which is mainly due to the decreasing costs and prices. This decline has been driven by the following:

An increasing efficiency of solar cells;

- Hanufacturing-technology improvements; and
- Economies of scale.

A PV inverter, which is an important element in the PV system, is used to convert dc power from the solar modules into ac power to be fed into the grid.

This project presents a multistring five-level inverter for PV application. The multistring inverter shown in Fig. 1 is a further development of the string inverter, where several strings are interfaced with their own dc–dc converter to a common dc–ac inverter. This is beneficial, compared with the centralized system, because every string can be controlled individually. Thus, the operator may start his/her own PV power plant with a few modules. Further enlargements are easily achieved because a new string with a dc–dc converter can be plugged into the existing platform.

A flexible design with high efficiency is hereby achieved. In this project, a nine-level inverter is used instead of a conventional three-level pulse widthmodulated (PWM) inverter because it offers great advantages, such as improved output waveforms, smaller filter size, lower electromagnetic interference, lower total harmonic distortion (THD), and other. An auxiliary circuit comprising four diodes and a switch is configured together with a conventional full-bridge inverter to form this topology.

A novel PWM control scheme is introduced to generate switching signals for the switches and to produce seven output-voltage levels: zero, +1/3Vdc, 2/3Vdc, Vdc, -1/2Vdc, and -1/3Vdc, -2/3Vdc, -Vdc, (assuming that Vdc is the supply voltage). This inverter topology uses two reference signals instead of one to generate PWM signals for the switches.



#### International Journal of Technology and Engineering System(IJTES) ISSN: 0976-1345 Vol5.No.1 2013 pp 41-44 available at: <u>www.ijcns.com</u> Paper Received :15-05-2013 Paper Received by: 1. Prof.S.Satya Janaki 2.Dr.Rajesh Kanna Editor : Prof. P.Muthukumar

### **II.SOLAR ARRAY'S**

Knowledge of the sun is very important in the optimization of photovoltaic systems. Solar energy is the most abundant renewable resource. The electromagnetic waves emitted by the sun are referred to as solar radiation. The amount of sunlight received by any surface on earth will depend on several factors including; geographical location, time of the day, season, local landscape and local weather. The light's angle of incidence on a given surface will depend on the orientation since the Earth's surface is round and the intensity will depend on the distance that the light has to travel to reach the respective surface. The radiation received by a surface will have two components one which is direct and will depend on the distance the rays travel (air mass). The other component is called diffuse radiation and is illustrated in figure 2.1. The range of wavelengths of light that reach the earth varies for 300nm to 400nm approximately. This is significantly different from

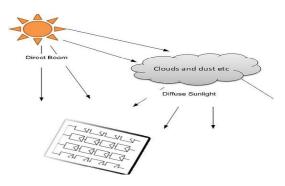


Fig:1Types of radiation from the sun

#### Nine level inverter

This project presents a high conversion ratio hybrid DC-DC converter fed single-phase low harmonic distortion nine-level photovoltaic (PV) inverter topology for PV power conditioning systems with a novel pulse widthmodulated (PWM) control scheme. A digital PI control algorithm is implemented in microcontroller PIC16C7F88 to keep the current injected into the grid sinusoidal and to have good dynamic performance with rapidly changing atmospheric conditions. For low-voltage dc energy sources, a power conditioning system (PCS) is needed to convert the energy sources to a higher-voltage dc before making it to ac for grid tie applications. Fuel cells and Solar photovoltaic (PV) are perhaps the most well-known and prospective energy sources with low voltage dc output. A thermoelectric generator, a battery, and an ultracapacitor are also examples of such low-voltage dc energy sources. Recently, numerous circuit topologies for the power conversion from low-voltage dc to high-voltage ac for grid-tie applications were proposed to deal with specific issues such as high efficiency, low cost, and safety.The whole system is constructed and simulated using MATLAB/SIMULINK.

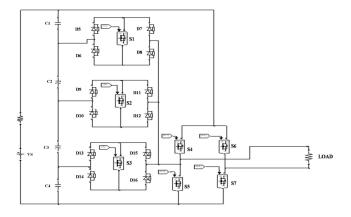


Fig:2 Nine level inverter

voltage source boosted by the dc–dc boost converters to exceed grid voltage Vg, and the voltage across the dc Combinations of PV strings are used as the input bus is known as *V*pv.

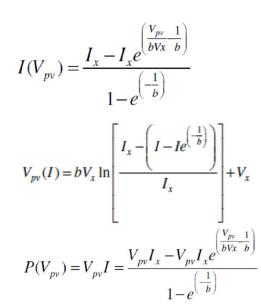
These operating principles are proposed inverter is to generate seven output-voltage level. As auxiliary circuit that consists of twelve diodes and a switch S1, S2, S3 is used between the dc-bus.Proper switching control of the auxiliary circuit can generate half level of PV supply voltage, four reference signals Vref1, Vref2, Vref3 and Vref4 will take turns to be compared with the carrier signal at a time. If Vref1 exceeds the peak amplitude of carrier signal Vcarrier, then Vref2 will be compared with the carrier signal until it reaches zero. Switch S2-S7 is on condition.[+Vs/2]. Switch S4-S7 is on condition.[+3Vs/4]. Repeated the process in negative side.



International Journal of Technology and Engineering System(IJTES) ISSN: 0976-1345 Vol5.No.1 2013 pp 41-44 available at: <u>www.ijcns.com</u> Paper Received :15-05-2013 Paper Reviewed by: 1. Prof.S.Satya Janaki 2.Dr.Rajesh Kanna Editor : Prof. P.Muthukumar

<b>S1</b>	\$2	\$3	<b>S4</b>	\$5	\$6	\$7	Vout
OFF	OFF	ON	OFF	OFF	OFF	ON	+VS
OFF	ON	OFF	OFF	OFF	OFF	ON	+2VS/4
ON	OFF	ĊFF	OFF	OFF	OFF	ON	+V3/2
OFF	OFF	OFF	ON	OFF	OFF	ON	+3V8/4
OFF	OFF	OFF	OFF	ON	OFF	ON	
Or	Or	Or	Or	Or	Or	Or	0
OFF	OFF	OFF	OFF	ON	OFF	ON	
OFF	OFF	OFF	OFF	ON	OFF	ON	-V3
ÓŇ	OFF	OFF	OFF	OFF	ÓŇ	OFF	-2\18/4
OFF	ON	OFF	OFF	OFF	ON	OFF	-V8/2
OFF	OFF	ON	OFF	OFF	ON	OFF	-3V8/4

### 9-Level Switching pattern



The output current of the PV panel is taking to be equal to the average input current of the DC-DC converter. Capacitor current is given by,

MATHEMATIC MODEL

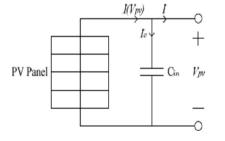
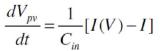


Fig 3.Dynamic PV panel.

The equations that describe the electric behavior of the PV panel under constant irradiance and temperature are:



Where I is the PV panel current, Vpv is the PV panel voltage, Vx is the PV panel open circuit voltage (Voc), Ix is the PV panel short circuit current (Isc), b is the PV panel characteristic constant and P is the PV panel output power.

Determining the derivative of the PV panel output power with respect to its voltage, gives the following:

## SIMULATION CIRCUIT OF NINE-LEVEL INVERTER



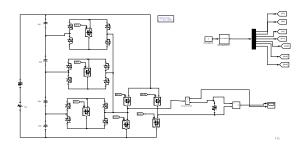


Fig.4 Simulation circuit of nine-level inverter

# SIMULATION OUTPUT FOR NINE-LEVEL INVERTER

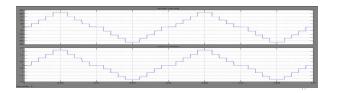


Fig.5 Simulation output for nine-level inverter

#### REFERENCES

[1] N. A. Rahim and S. Mekhilef, "Implementation of three-phase grid connected inverter for photovoltaic solar power generation system," in *Proc. IEEE PowerCon*, Oct. 2002, vol. 1, pp. 570–573.

[2] J. M. Carrasco, L. G. Franquelo, J. T. Bialasiewicz, E. Galvan, R. C. PortilloGuisado, M. A. M. Prats, J. I. Leon, and N.Moreno-Alfonso, "Power-electronic systems for the grid integration of renewable energy sources: A survey," *IEEE Trans. Ind. Electron.*, vol. 53, no. 4, pp. 1002–1016, Aug. 2006.

[3] S. B. Kjaer, J. K. Pedersen, and F. Blaabjerg, "A review of single-phase grid connected inverters for photovoltaic modules," *IEEE Trans. Ind. Appl.*, vol. 41, no. 5, pp. 1292–1306, Sep./Oct. 2005.

[4] S. Daher, J. Schmid, and F. L.M. Antunes, "Multilevel inverter topologies for stand-alone PV systems," *IEEE Trans. Ind. Electron.*, vol. 55, no. 7, pp. 2703–2712, Jul. 2008.

[5] M. Meinhardt and G. Cramer, "Past, present and future of grid-connected photovoltaic- and hybrid-powersystems," in *Proc. IEEE-PES Summer Meeting*, Jul. 2000, vol. 2, pp. 1283–1288.

[6] S. Kouro, J. Rebolledo, and J. Rodriguez, "Reduced switching-frequencymodulation algorithm for high-power multilevel inverters," *IEEE Trans. Ind. Electron.*, vol. 54, no. 5, pp. 2894–2901, Oct. 2007.

[7] S. J. Park, F. S. Kang, M. H. Lee, and C. U. Kim, "A new single-phase fivelevel PWM inverter employing a deadbeat control scheme," *IEEE Trans. Power Electron.*, vol. 18, no. 18, pp. 831–843, May 2003.

[8] L. M. Tolbert and T. G. Habetler, "Novel multilevel inverter carrier-based PWM method," *IEEE Trans. Ind. Appl.*, vol. 35, no. 5, pp. 1098–1107, Sep./Oct. 1999.

[9] Y. Liu, H. Hong, and A. Q. Huang, "Real-time calculation of switching angles minimizing THD for multilevel inverters with step modulation,"*IEEE Trans. Ind. Electron.*, vol. 56, no. 2, pp. 285–293, Feb. 2009.

[10] N. S. Choi, J. G. Cho, and G. H. Cho, "A general circuit topology of multilevel inverter," in *Proc. IEEE 22th Annu. PESC*, Jun. 24–27, 1991,pp. 96–103